

Amendments to the Claims:

Please cancel claims 1-49

Listing of Claims:

1-49. (Cancelled)

50. (Original) A computer system, comprising:

a data input device;

a data output device;

a processor coupled to the data input and output devices; and

a memory device including a chip package having a plurality of conductors coupled to the processor, the memory device including,

a chip including memory circuitry, the memory circuitry including,

an address decoder coupled to an address bus;

a read/write circuit coupled to a data bus;

a control circuit coupled to a control bus;

a memory-cell array coupled to the address decoder, control circuit, and read/write circuit, the memory-cell array; and

a first converter coupled to the address, data, and control busses, the first converter operable to data signals on the data bus and convert the data signals into corresponding data output electromagnetic waves, and operable to receive address, data, and control electromagnetic waves and convert these electromagnetic waves into corresponding electric address, data, and control signals that are applied on the address, data, and control busses, respectively; and

a chip package physically coupled to the chip, the chip package including a second converter that is operable to receive the data output electromagnetic waves from the first converter and convert these received electromagnetic waves into corresponding electric data output signals that are applied to corresponding conductors, and the second

converter operable to receive electric address, data, and control signals on corresponding conductors and to convert these electric signals into corresponding address, data, and control electromagnetic waves that are communicated to the first converter.

51. (Cancelled) The computer system of claim 50 wherein the electromagnetic waves comprise optical electromagnetic signals.

52. (Cancelled) The computer system of claim 50 wherein the electromagnetic waves comprise infrared electromagnetic waves.

53. (Cancelled) The computer system of claim 50 wherein the first and second converters comprise laser diodes.

54. (Cancelled) The computer system of claim 50 wherein the memory device comprises a dynamic random access memory.